



Bulk Metal[®] Foil Technology

16 Pin Transistor Outline Hermetic Resistor Network



Product may not be to scale

The 16 Pin TO-8 package is suitable for ladder networks up to 12 bits and other more complicated networks. It is also a good choice when power dissipation is a consideration. This network can contain up to 49 V5X5 resistor chips.

Review data sheet "7 Technical Reasons to Specify Bulk Metal[®] Foil Resistor Networks."

ORDERING INFORMATION - 1422 PARTS

Networks are built to your requirements. Send your schematic and electrical requirements to the Applications Engineering Department. (See data sheet "Network Worksheet.") A unique part number will be assigned which defines all aspects of your network.

FIGURE 1 - STANDARD DIMENSIONS in inches (millimeters)

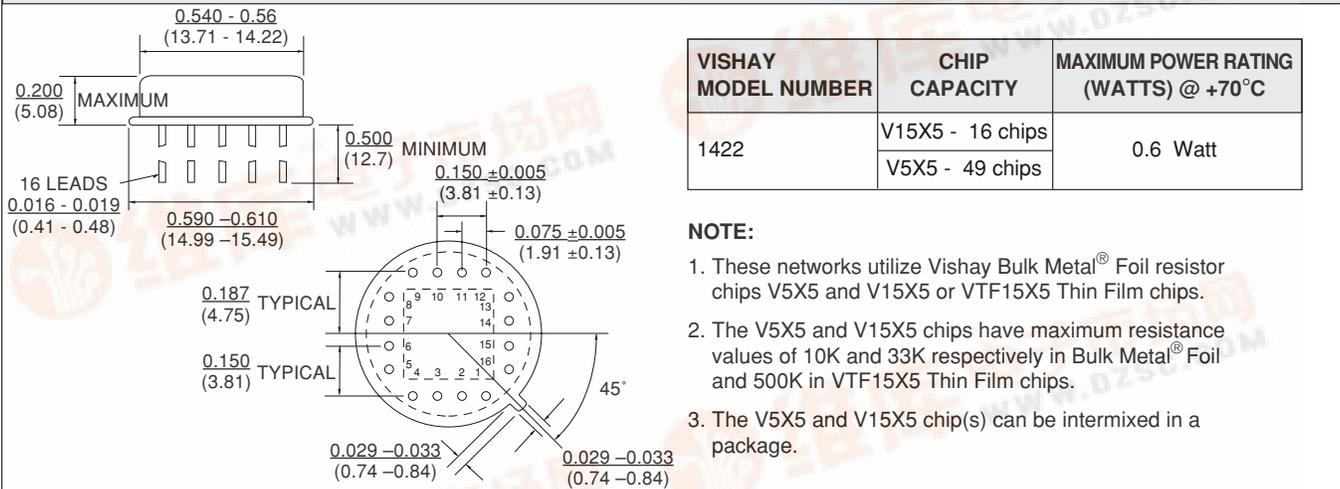
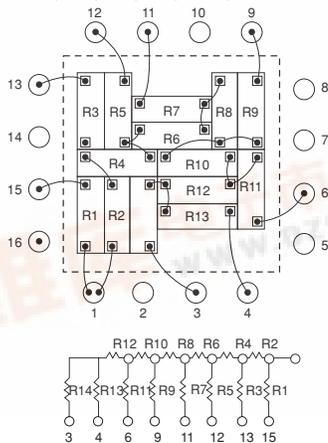


FIGURE 2 - SAMPLE CIRCUIT DESIGNS AND CHIP LAYOUTS

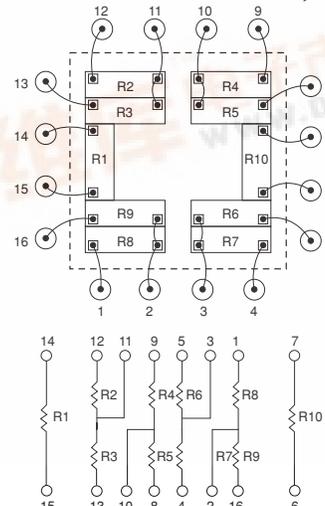
NOTE: Usable area is represented by dotted lines—a square 0.350 Inches x 0.350 Inches. Illustrations not to scale. Chips shown undersize for clarity. Drawing view is from the top looking down into the package.

7 BIT R/2R LADDER

R1, R3, R5, R7, R9, R11, R13, R14 = 2R
R2, R4, R6, R8, R10, R12 = R



4 DIVIDERS PLUS APPLICATION RESISTORS FOR DIFFERENTIAL OP AMPS, ETC.



THROUGH HOLE





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