



**Advanced Power  
Electronics Corp.**

**AP2N7002K**

**Pb Free Plating Product**

*N-CHANNEL ENHANCEMENT MODE*

*POWER MOSFET*

- ▼ Simple Drive Requirement
- ▼ Small Package Outline
- ▼ Surface Mount Device
- ▼ RoHS Compliant

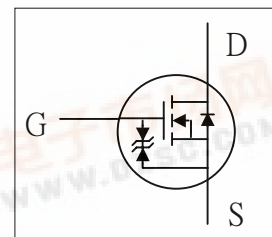


|              |       |
|--------------|-------|
| $BV_{DSS}$   | 60V   |
| $R_{DS(ON)}$ | 2Ω    |
| $I_D$        | 640mA |

### Description

Advanced Power MOSFETs utilized advanced processing techniques to achieve the lowest possible on-resistance, extremely efficient and cost-effectiveness device.

The SOT-23 package is universally used for all commercial-industrial applications.



### Absolute Maximum Ratings

| Symbol                 | Parameter  | Rating     | Units |
|------------------------|--|------------|-------|
| $V_{DS}$               | Drain-Source Voltage                                   | 60         | V     |
| $V_{GS}$               | Gate-Source Voltage                                    | ±20        | V     |
| $I_D @ T_A=25^\circ C$ | Continuous Drain Current <sup>3</sup> , $V_{GS}$ @ 10V | 640        | mA    |
| $I_D @ T_A=70^\circ C$ | Continuous Drain Current <sup>3</sup> , $V_{GS}$ @ 10V | 500        | mA    |
| $I_{DM}$               | Pulsed Drain Current <sup>1,2</sup>                    | 950        | mA    |
| $P_D @ T_A=25^\circ C$ | Total Power Dissipation                                | 1.38       | W     |
|                        | Linear Derating Factor                                 | 0.01       | W/°C  |
| $T_{STG}$              | Storage Temperature Range                              | -55 to 150 | °C    |
| $T_J$                  | Operating Junction Temperature Range                   | -55 to 150 | °C    |

### Thermal Data

| Symbol | Parameter  | Value   | Unit |
|--------|--|---------|------|
| Rthj-a | Thermal Resistance Junction-ambient <sup>3</sup> | Max. 90 | °C/W |





**Electrical Characteristics @T<sub>j</sub>=25°C(unless otherwise specified)**

| Symbol                              | Parameter   | Test Conditions  | Min. | Typ. | Max. | Units |
|-------------------------------------|---|--|------|------|------|-------|
| BV <sub>DSS</sub>                   | Drain-Source Breakdown Voltage                      | V <sub>GS</sub> =0V, I <sub>D</sub> =250uA               | 60   | -    | -    | V     |
| ΔBV <sub>DSS</sub> /ΔT <sub>j</sub> | Breakdown Voltage Temperature Coefficient           | Reference to 25°C, I <sub>D</sub> =1mA                   | -    | 0.06 | -    | V/°C  |
| R <sub>DS(ON)</sub>                 | Static Drain-Source On-Resistance                   | V <sub>GS</sub> =10V, I <sub>D</sub> =500mA              | -    | -    | 2    | Ω     |
|                                     |   | V <sub>GS</sub> =4.5V, I <sub>D</sub> =200mA             | -    | -    | 4    | Ω     |
| V <sub>GS(th)</sub>                 | Gate Threshold Voltage                              | V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA | 1    | -    | 2.5  | V     |
| g <sub>fs</sub>                     | Forward Transconductance                            | V <sub>DS</sub> =10V, I <sub>D</sub> =600mA              | -    | 600  | -    | mS    |
| I <sub>DSS</sub>                    | Drain-Source Leakage Current (T <sub>j</sub> =25°C) | V <sub>DS</sub> =60V, V <sub>GS</sub> =0V                | -    | -    | 10   | uA    |
|                                     | Drain-Source Leakage Current (T <sub>j</sub> =70°C) | V <sub>DS</sub> =48V, V <sub>GS</sub> =0V                | -    | -    | 100  | uA    |
| I <sub>GSS</sub>                    | Gate-Source Leakage                                 | V <sub>GS</sub> =±20V                                    | -    | -    | ±30  | uA    |
| Q <sub>g</sub>                      | Total Gate Charge <sup>2</sup>                      | I <sub>D</sub> =600mA                                    | -    | 1    | 1.6  | nC    |
| Q <sub>gs</sub>                     | Gate-Source Charge                                  | V <sub>DS</sub> =50V                                     | -    | 0.5  | -    | nC    |
| Q <sub>gd</sub>                     | Gate-Drain ("Miller") Charge                        | V <sub>GS</sub> =4.5V                                    | -    | 0.5  | -    | nC    |
| t <sub>d(on)</sub>                  | Turn-on Delay Time <sup>2</sup>                     | V <sub>DS</sub> =30V                                     | -    | 12   | -    | ns    |
| t <sub>r</sub>                      | Rise Time   | I <sub>D</sub> =600mA                                    | -    | 10   | -    | ns    |
| t <sub>d(off)</sub>                 | Turn-off Delay Time                                 | R <sub>G</sub> =3.3Ω, V <sub>GS</sub> =10V               | -    | 56   | -    | ns    |
| t <sub>f</sub>                      | Fall Time   | R <sub>D</sub> =52Ω                                      | -    | 29   | -    | ns    |
| C <sub>iss</sub>                    | Input Capacitance                                   | V <sub>GS</sub> =0V                                      | -    | 32   | 50   | pF    |
| C <sub>oss</sub>                    | Output Capacitance                                  | V <sub>DS</sub> =25V                                     | -    | 8    | -    | pF    |
| C <sub>rss</sub>                    | Reverse Transfer Capacitance                        | f=1.0MHz   | -    | 6    | -    | pF    |

**Source-Drain Diode**

| Symbol          | Parameter                       | Test Conditions                           | Min. | Typ. | Max. | Units |
|-----------------|---------------------------------|---|------|------|------|-------|
| V <sub>SD</sub> | Forward On Voltage <sup>2</sup> | I <sub>S</sub> =1.2A, V <sub>GS</sub> =0V | -    | -    | 1.2  | V     |

**Notes:**

- 1.Pulse width limited by Max. junction temperature.
- 2.Pulse width ≤300us , duty cycle ≤2%.
- 3.Surface mounted on 1 in<sup>2</sup> copper pad of FR4 board ; 270°C/W when mounted on min. copper pad.

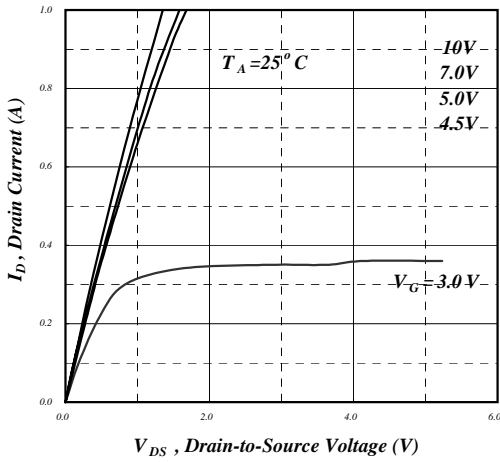


Fig 1. Typical Output Characteristics

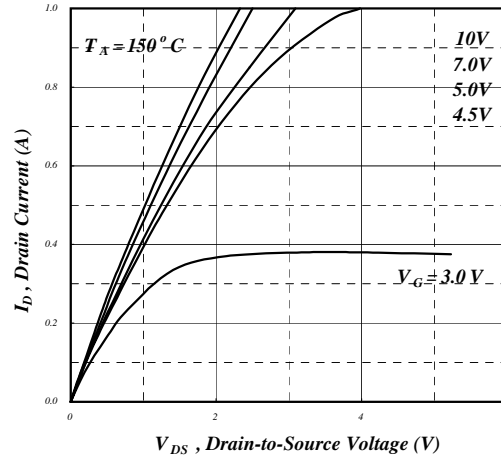


Fig 2. Typical Output Characteristics

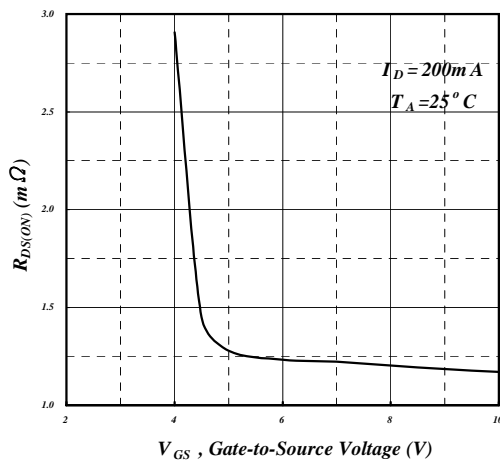


Fig 3. On-Resistance v.s. Gate Voltage

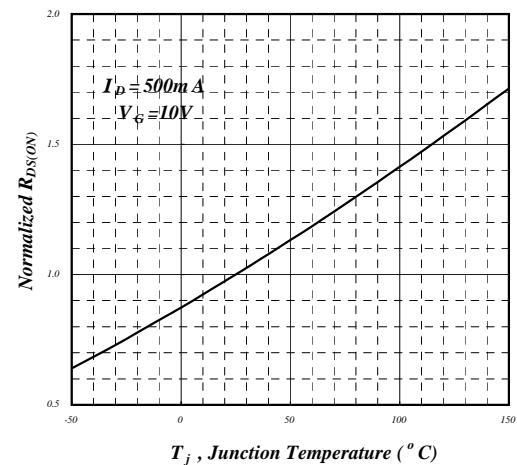


Fig 4. Normalized On-Resistance v.s. Junction Temperature

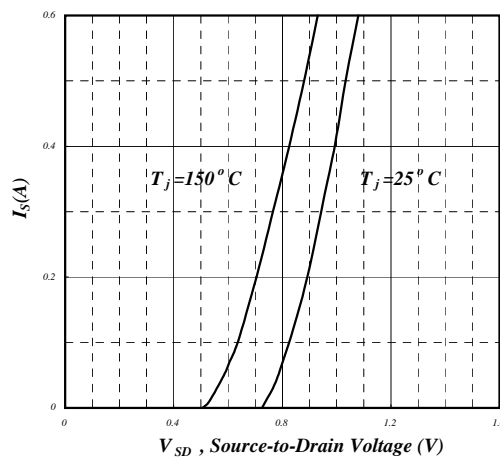


Fig 5. Forward Characteristic of Reverse Diode

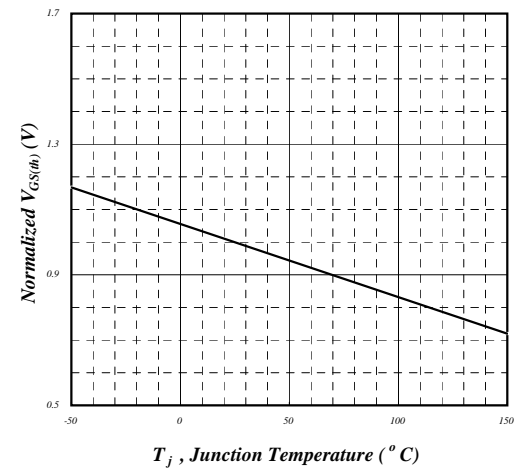


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

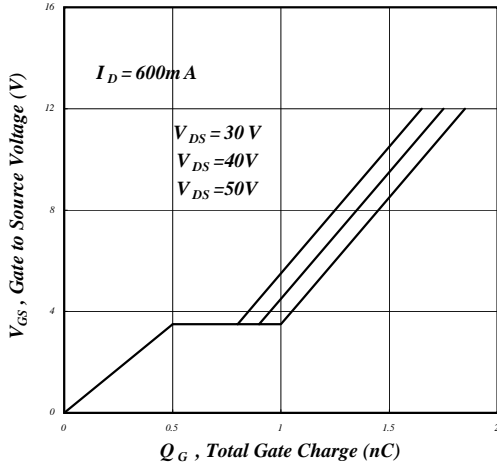


Fig 7. Gate Charge Characteristics

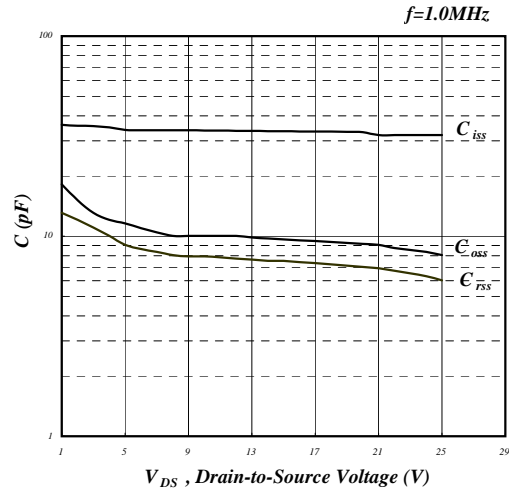


Fig 8. Typical Capacitance Characteristics

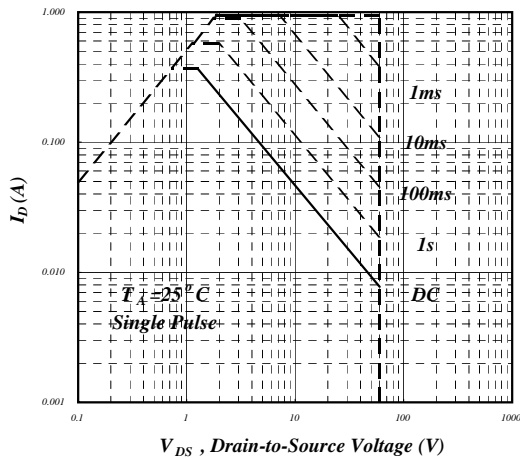


Fig 9. Maximum Safe Operating Area

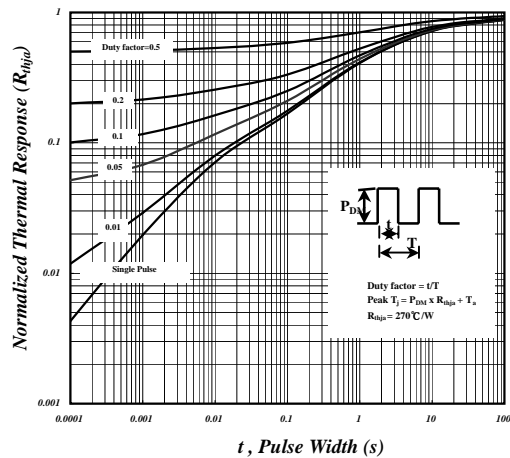


Fig 10. Effective Transient Thermal Impedance

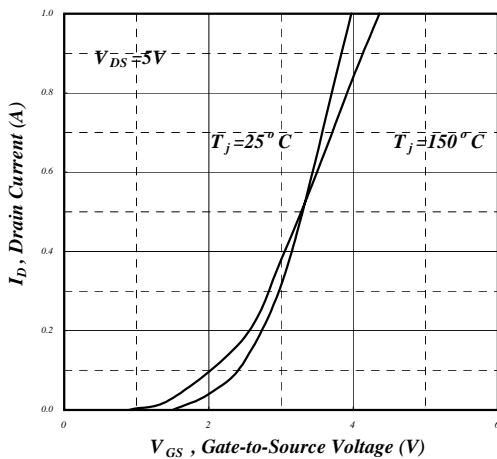


Fig 11. Transfer Characteristics

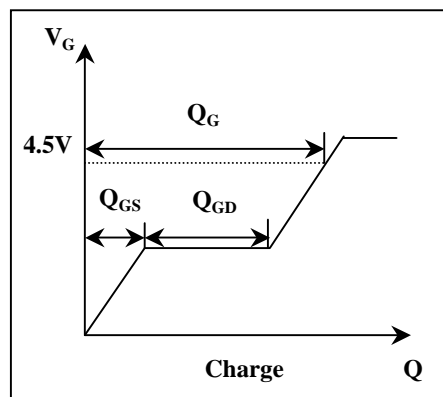


Fig 12. Gate Charge Waveform